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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/821,357	04/09/2004	Gregory D. Miller	10021.000201 (P0177-D) 1202 EXAMINER	
31894 OK AMOTO &	7590 12/11/2007 E BENEDICTO, LLP			
P.O. BOX 641330			ROSENAU, DEREK JOHN	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)			
		MILLER ET AL.			
Office Action Summary	10/821,357	Art Unit			
omee Action Gammary	Examiner				
The MAILING DATE of this communication app	Derek J. Rosenau	2834 orrespondence address			
Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DATE of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  If NO period for reply is specified above, the maximum statutory period was reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be timudil apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE.	<b>J</b> . nely filed the mailing date of this communication. D (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 15 O	<u>ctober 2007</u> .				
,-					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims					
4) ☐ Claim(s) 17-33 is/are pending in the application 4a) Of the above claim(s) is/are withdraw 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 17-33 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/o	wn from consideration.				
Application Papers					
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) accomplicated any not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Example.	epted or b) objected to by the drawing(s) be held in abeyance. Section is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>					
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO/SB/08)  Paper No(s)/Mail Date 10/10/2006.	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	ate			

#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 17, 19-25, 31 and 32 are rejected under 35 U.S.C. 103(a) as being unpatentable over Pahl et al. (US 6931699) in view of Gupta et al. (US 5955659) and Tsuzuki et al. (US 20030127943).
- 3. With respect to claim 17, Pahl et al. discloses a surface acoustic wave (SAW) device sealed at the wafer level (Figures 1-7), the device comprising: an active area to be protected (item 5); an electrical contact area (item 10); and a lithographically-formed (column 4, lines 11-16) structure sealing at least the active area and leaving at least a portion of the electrical contact area exposed (Figures 6 and 7).

Pahl et al. does not disclose expressly that the lithographically-formed structure comprises a seal coating deposited over a sacrificial material, said sacrificial material being subsequently removed by etching, or that the sealing structure hermetically seals the active area.

Gupta et al. teaches a lithographically-formed structure (Fig 11a-11e) in which a seal coating (item 231) is deposited over a sacrificial material (item 208), said sacrificial material being subsequently removed by etching (column 7, line 56 through column 89, line 10).

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Tsuzuki et al. teaches a SAW device in which the active area is hermetically sealed (Abstract).

At the time of invention, it would have been obvious to a person of ordinary skill in the art to combine the lithographically-formed seal coating of Gupta et al. with the SAW device of Pahl et al. for the benefit of forming the seal coating as a single piece and to combine the hermetic seal of Tsuzuki et al. with the SAW device of Pahl et al. for the benefit of improving the quality of the seal by providing an airtight seal.

- 4. With respect to claim 19, the combination of Pahl et al. and Gupta discloses the device of claim 17. Pahl et al. discloses that the SAW device is fabricated on a substrate from a group consisting of lithium tantalite, lithium niobate, and quartz (column 3, lines 48-50).
- 5. With respect to claim 20, Pahl et al. discloses a lithographically-fabricated surface acoustic wave (SAW) device (Figs 1-7), the SAW device comprising: means for carrying a surface acoustic wave (item 1); and a wafer-level means for sealing the means for carrying the surface acoustic wave (items 7-9).

Pahl et al. does not disclose expressly that said wafer-level means for sealing comprises a seal coating deposited over a sacrificial material, said sacrificial material being subsequently removed by etching, or that the sealing structure hermetically seals the active area.

Gupta et al. teaches a lithographically-formed structure (Fig 11a-11e) in which the wafer-level means for sealing comprises a seal coating (item 231) deposited over a

sacrificial material (item 208), said sacrificial material being subsequently removed by etching (column 7, line 56 through column 89, line 10).

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At the time of invention, it would have been obvious to a person of ordinary skill in the art to combine the lithographically-formed seal coating of Gupta et al. with the SAW device of Pahl et al. for the benefit of forming the seal coating as a single piece and to combine the hermetic seal of Tsuzuki et al. with the SAW device of Pahl et al. for the benefit of improving the quality of the seal by providing an airtight seal.

- 6. With respect to claim 21, the combination of Pahl et al. and Gupta et al. discloses the SAW device of claim 20. Pahl et al. discloses that the means for carrying the surface acoustic wave comprises a transducer structure (items 2a and 2b).
- 7. With respect to claim 22, the combination of Pahl et al. and Gupta et al. discloses the SAW device of claim 21. Pahl et al. discloses that the transducer structure comprises aluminum (column 3, lines 55 and 56) patterned into interdigitated electrode fingers (items 2a and 2b).
- 8. With respect to claim 23, the combination of Pahl et al. and Gupta et al. discloses the SAW device of claim 20. Pahl et al. discloses that the wafer-level means for sealing comprises a lithographically-formed structure sealing at least the means for carrying (Figures 6 and 7).
- 9. With respect to claim 24, the combination of Pahl et al. and Gupta et al. discloses the SAW device of claim 23. Pahl et al. discloses electrical contact areas coupled to the

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means for carrying, and wherein the wafer-level means for sealing leaves exposed at least a portion of the electrical contact areas (item 10).

- 10. With respect to claim 25, the combination of Pahl et al. and Gupta et al. discloses the device of claim 17. Pahl et al. discloses that the lithographically-formed structure comprises a material of a thickness so as to be impermeable to undesired contaminants (column 4, lines 42-47).
- 11. With respect to claim 31, the combination of Pahl et al. and Gupta et al. discloses the device of claim 17. Pahl et al. discloses that the SAW device is fabricated on a lithium tantalite substrate (column 3, lines 48-50).
- 12. With respect to claim 32, the combination of Pahl et al. and Gupta et al. discloses the device of claim 17. Pahl et al. discloses that the SAW device is fabricated on a lithium niobate substrate (column 4, lines 48-50).
- 13. Claims 18, 26-28, and 33 are rejected under 35 U.S.C. 103(a) as being unpatentable over Pahl et al. in view of Gupta et al., Tsuzuki et al., and Onishi et al. (US 6154940).
- 14. With respect to claim 18, the combination of Pahl et al., Gupta et al., and Tsuzuki et al. discloses the device of claim 17.

None of Pahl et al., Gupta et al., or Tsuzuki et al. disclose expressly that the lithographically-formed structure comprises a glassy material.

Onishi et al. teaches a SAW device that uses a glassy material to seal the active area (column 7, lines 43-51).

At the time of invention, it would have been obvious to a person of ordinary skill in the art to combine the glassy-material of Onishi et al. with the SAW device of Pahl et al. as modified by Gupta et al. and Tsuzuki et al. in order make the device more compatible with common electronics manufacturing processes.

15. With respect to claim 26, the combination of Pahl et al., Gupta et al., and Tsuzuki et al. discloses the device of claim 17.

None of Pahl et al., Gupta et al., or Tsuzuki et al. disclose expressly that the lithographically-formed structure comprises silicon dioxide.

Onishi et al. teaches a SAW device that uses silicon dioxide to seal the active area (column 7, lines 43-51).

At the time of invention, it would have been obvious to a person of ordinary skill in the art to combine the silicon dioxide of Onishi et al. with the SAW device of Pahl et al. as modified by Gupta et al. and Tsuzuki et al. in order make the device more compatible with common electronics manufacturing processes.

16. With respect to claim 27, the combination of Pahl et al., Gupta et al., and Tsuzuki et al. discloses the device of claim 17.

None of Pahl et al., Gupta et al., or Tsuzuki et al. disclose expressly that the lithographically-formed structure comprises silicon nitride.

Onishi et al. teaches a SAW device that uses silicon nitride to seal the active area (column 7, lines 43-51).

At the time of invention it would have been obvious to a person of ordinary skill in the art to combine the silicon nitride of Onishi et al. with the SAW device of Pahl et al.

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as modified by Gupta et al. and Tsuzuki et al. in order make the device more compatible with common electronics manufacturing processes.

17. With respect to claim 28, the combination of Pahl et al., Gupta et al., and Tsuzuki et al. discloses the device of claim 17.

None of Pahl et al., Gupta et al., or Tsuzuki et al. disclose expressly that the lithographically-formed structure comprises a metal.

Onishi et al. teaches a SAW device that uses a metal to seal the active area (column 7, lines 43-51).

At the time of invention it would have been obvious to a person of ordinary skill in the art to combine the metal of Onishi et al. with the SAW device of Pahl et al. as modified by Gupta et al. and Tsuzuki et al. in order make the device more compatible with common electronics manufacturing processes.

18. With respect to claim 33, the combination of Pahl et al., Gupta et al., and Tsuzuki et al. discloses the device of claim 17.

None of Pahl et al., Gupta et al., or Tsuzuki et al. disclose expressly that the SAW device is fabricated on a quartz substrate.

Onishi et al. teaches a SAW device fabricated on a quartz substrate (column 15, lines 41-43).

At the time of invention it would have been obvious to a person of ordinary skill in the art to combine the quartz substrate of Onishi et al. with the SAW device of Pahl et al. as modified by Gupta et al. and Tsuzuki et al. in order make the device more compatible with common electronics manufacturing processes.

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19. Claim 29 is rejected under 35 U.S.C. 103(a) as being unpatentable over Pahl et al. in view of Gupta et al., Tsuzuki et al., and Bashir et al. (US 6716620).

20. With respect to claim 29, the combination of Pahl et al., Gupta et al., and Tsuzuki et al. discloses the device of claim 18.

None of Pahl et al., Gupta et al., or Tsuzuki et al. disclose expressly that the glassy material comprises a spin-on-glass.

Bashir et al teaches an electronic device in which the chip is sealed by spin-on-glass (column 7, lines 51-57).

At the time of invention, it would have been obvious to a person of ordinary skill in the art to combine the spin-on-glass of Bashir et al. with the SAW device of Pahl et al. as modified by Gupta et al. and Tsuzuki et al. in order to make the device more compatible with common manufacturing processes.

- 21. Claim 30 is rejected under 35 U.S.C. 103(a) as being unpatentable over Pahl et al. in view of Gupta et al., Tsuzuki et al., and Orcutt et al. (US 6743656).
- 22. With respect to claim 30, the combination of Pahl et al., Gupta et al., and Tsuzuki et al. discloses the device of claim 18.

None of Pahl et al., Gupta et al., or Tsuzuki et al. disclose expressly that the glassy material comprises a sputtered glass.

Orcutt et al teaches an electronic device in which the chip is sealed by sputtered glass (column 4, line 67 through column 5, line 2).

At the time of invention, it would have been obvious to a person of ordinary skill in the art to combine the spin-on-glass of Orcutt et al. with the SAW device of Pahl et al.

as modified by Gupta et al. and Tsuzuki et al. in order to make the device more compatible with common manufacturing processes.

## Response to Arguments

- 23. Applicant's arguments filed 15 October 2007 have been fully considered but they are not persuasive. Applicant argues that Onishi et al. does not teach a glassy material. However, Onishi et al. does disclose the use of silicon oxide, which is a glassy material, as glass is largely silicon oxide.
- 24. Applicant's arguments with respect to claims 17-33 have been considered but are moot in view of the new ground(s) of rejection.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Derek J. Rosenau whose telephone number is 571-272-8932. The examiner can normally be reached on Monday thru Thursday 7:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Darren Schuberg can be reached on 571-272-2044. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Derek J Rosenau Examiner Art Unit 2834

DJR 12/6/2007

